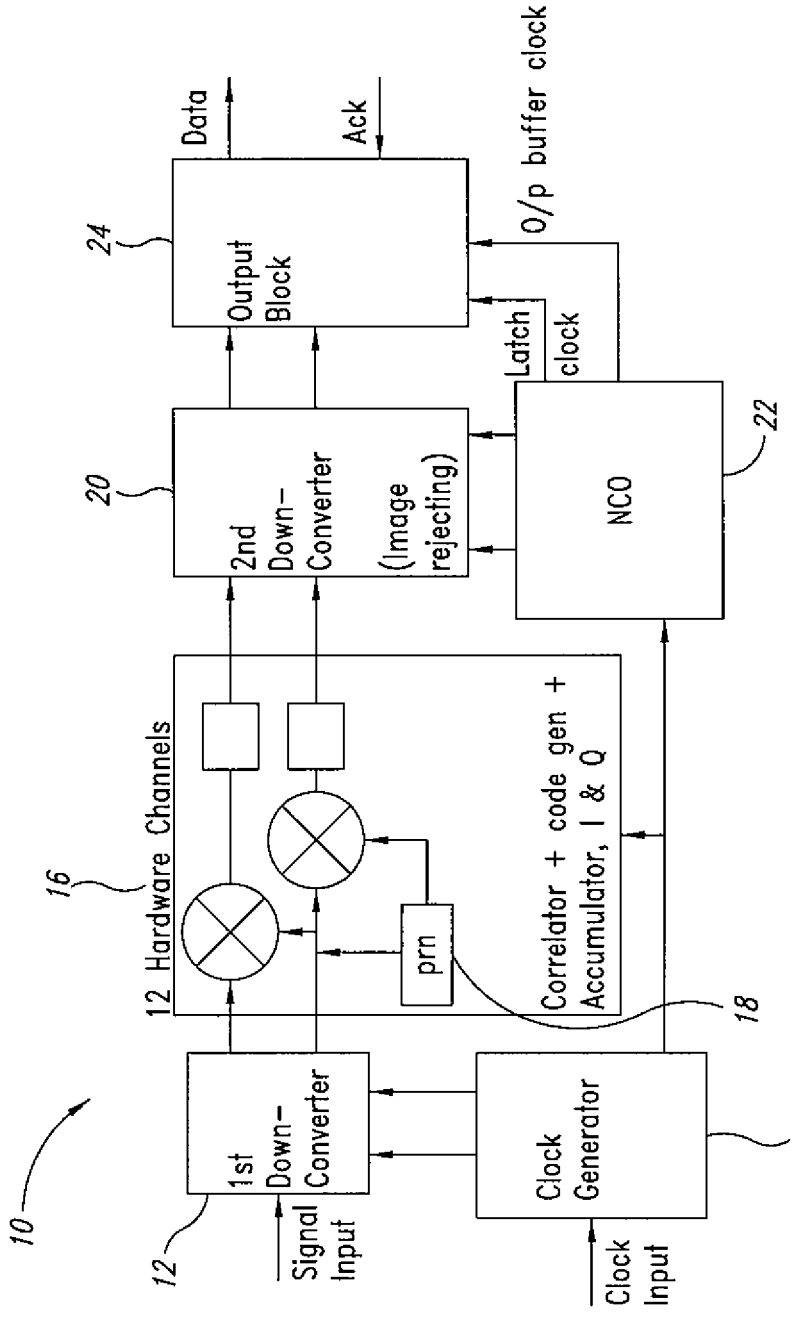
Data
Ack
O/p buffer clock
24
Output Block
Latch clock
20
2nd Down-Converter (Image rejecting)
NCO
22
16
12 Hardware Channels
prn
Correlator + code gen + Accumulator, I & Q
18
10
12
1st Down-Converter
Signal Input
Clock Generator
14
Clock Input

FIG. 2